

**MARKED UP VERSION OF REWRITTEN CLAIMS**

13. (Amended) A memory system according to claim 12 [or 13], wherein both said second terminal and said third terminal are a data terminal.

14. (Amended) A memory system according to claim 12 [or 13],

wherein said connecting member comprises a sixth terminal,

wherein said board further comprises a first board wiring that connects said first terminal with said sixth terminal, and

wherein said connecting member further comprises a first wiring member connected between said sixth terminal and said second terminal, and a second wiring member connected between said sixth terminal and said third terminal.

27. (Amended) A connecting member according to claim[s] 25 [and 26],

wherein said first mounting part and said first wiring member are a first socket pin, and

wherein said second mounting part and said second wiring member are a second socket pin.

29. (Amended) A connecting member according to claim[s]  
25 [to 28], wherein both said first terminal and said second  
terminal are a data terminal.

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